logan hutcheson

(706) 714-0527 ◇ loganchutcheson@gmail.com
loganhutcheson.com ◇ github ◇ linkedin

experience

software engineer, Cisco Systems **jun 2026 – present**
Research Triangle Park, NC
· brought up NVIDIA ConnectX-8 NICs on Grace servers; debugged PCIe, drivers, and Ethernet.
· integrated DPDK workloads and built unattended nightly and pre-commit router validation.

software engineer, Cisco Systems **feb 2021 – may 2025**
San Francisco Bay Area, CA
· firmware lead for CW9162, CW9163, and CW9179 Wi-Fi 6E/7 access points, from Qualcomm platform bring-up through manufacturing handoff.
· developed Linux drivers and U-Boot components; shipped crash-dump collection with Qualcomm.
· maintained OpenWrt and Yocto/BitBake builds.

software engineer intern, Cisco Systems **may – aug 2020**
· built unit-testing infrastructure and automation APIs for a Docker-based test platform.

ORISE research fellow, U.S. EPA **oct 2017 – may 2020**
· conducted metabolomics research and maintained NMR instrumentation.

selected project

jetson posture system C++20 / Python
· integrated CSI cameras, I^2C sensors, OLED, and GPIO feedback on Jetson Orin Nano.
· built a concurrent sensor runtime and Python pose pipeline with SQLite history and systemd services.

skills

C, C++, Python, Shell, Go; Linux, U-Boot, OpenWrt, Yocto/BitBake.
PCIe, I^2C, JTAG; Ethernet, DPDK, LLDP/CDP, PoE.
GDB, Wireshark, tcpdump, Git, Docker. CWNA certified.

education

University of Georgia, honors program 2016 – 2020
B.S. computer science ◇ B.S. biology ◇ Phi Beta Kappa